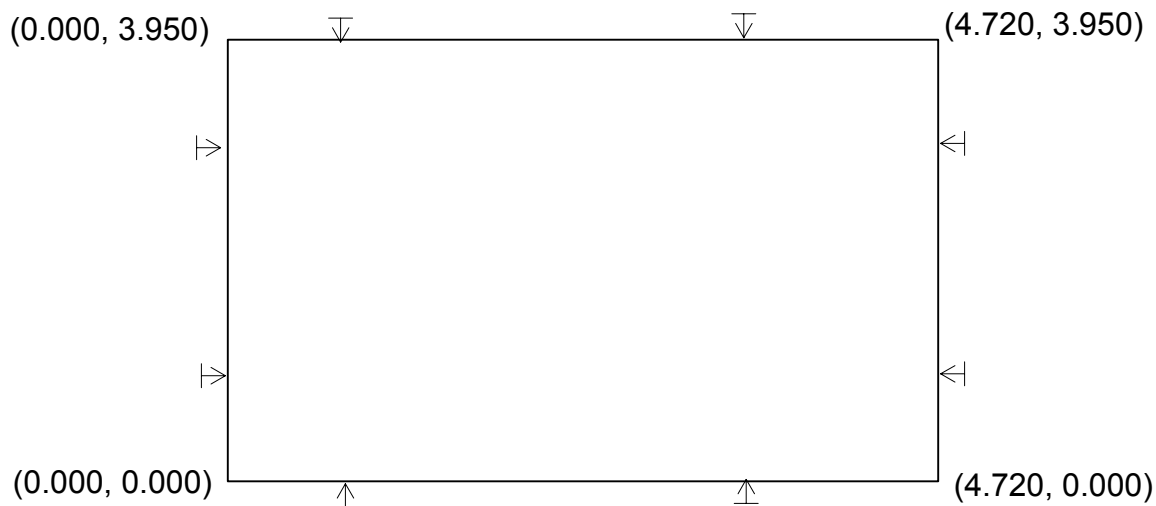



Penny Lane PCB Rev A, Cover Sheet
L Johnson, KK7P
2010 June 07 [Monday]



PCB Material FR4, thickness 0.062" nominal, 1 ounce copper, 4 layers, white silkscreen, green soldermask. There is no gold plating, unless otherwise required.

Stackup order: SST, SMT, TOP, PWR, GND, BOT, SMB, SSB.

Datum is the lower left corner of the PCB as shown in the drawing above. The provided Gerber and Excellon files share this same datum.

 This symbol represents the approximate break-off tab locations if the boards are panelized.

There are two (2) non plated holes 0.116" diameter at coordinates (0.215, 0.110) and (3.715, 0.110).

All other holes to be plated through.

[end]